FIG. 1 (PRIOR ART)

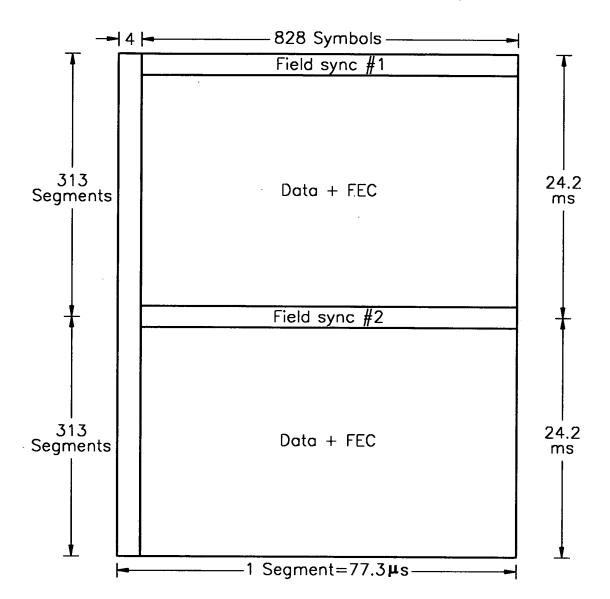
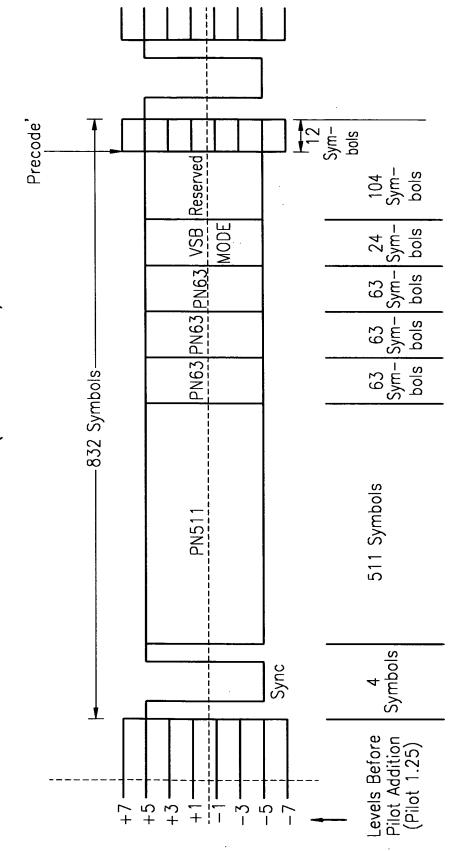


FIG. 2 (PRIOR ART)



...

360 > 300 FEC - SYNC SIGNALS .350 PT 340 EQUALIZER SYNC SIGNAL DETECTION CIRCUIT FIG. 3 (PRIOR ART) 370 DATA2 .330 NRF DATA1 DEMODULATOR .310 AGC ADC

FIELD SYNC SIGNAL 370 FIELD SYNC SIGNAL GENERATOR 425 445 FIRST COUNTER FIG. 4 (PRIOR ART) ,420 440 MAXIMUM VALUE DETECTOR SECOND COUNTER 415 COMPARATOR 435 455 SIGN GENERATOR BUFFER FOUR-SYMBOL SLIDING CORRELATOR PN 511 CORRELATOR PN 63 CORRELATOR , 410 430 450 DATA1.

. :

832 1 2 3 4 even field(313 segment) field sync segment 515 PN511 515 832 1 2 3 4 even field(313 segment) FIG. 5 (PRIOR ART) field sync segment 832 1 2 3 4 832 1 2 3 4 313 segment field sync segment 515 PN511 (C)FIELD SYNC (B) SEG SYNC (D)FIELD 313S  $\overline{\mathfrak{C}}$ 

